

Sample Question Paper

Curriculum Scheme: Revised 2016

Examination: Third Year Semester V Computer

Course Code: CSC401 Course Name: Microprocessor

Time: 2-hour

Max. Marks: 80

NB:- 1) Que1 will be 20 MCQ each of 2 Marks.

2) Que2 will be descriptive with 6 options out of which solve any 4 each of 5 Marks.

3) Que3 will be also descriptive with 3 options out of which solve any 2 each of 10 Marks.

Que1 MCQ .	
1.	How much is the frequency of 8086 processor.
Option A:	3 MHz
Option B:	5 MHz
Option C:	8 MHz
Option D:	10 MHz
2.	Bus Interface Unit (BIU) is responsible for_____ .
Option A:	Communicating with IO devices only
Option B:	Communicating with Memory only
Option C:	Communicating with IO devices and Memory
Option D:	Communicating with Input devices only
3.	Which are operating modes of 8086 processor.
Option A:	Real Mode & Protected Mode
Option B:	Minimum Mode & Maximum Mode
Option C:	Maximum Mode & Protected Mode
Option D:	Protected Mode & Virtual Mode
4.	How many chips of IC8282 are required in minimum mode of 8086?
Option A:	5
Option B:	4
Option C:	3
Option D:	2
5.	How much is the size of Instruction Prefetch Queue in 8086 processor?
Option A:	1
Option B:	2
Option C:	4

Option D:	6
6.	Which Instruction is from Immediate addressing mode ?
Option A:	MOV AX, BX
Option B:	MOV AX, [BX]
Option C:	MOV AX, [1234H]
Option D:	MOV AX, 1234H
7.	DIV BX instruction store the result in ____ register?
Option A:	BX
Option B:	AX
Option C:	AX_DX
Option D:	DX_AX
8.	Which segment is used as destination segment in string instruction?
Option A:	ES
Option B:	SS
Option C:	CS
Option D:	DS
9.	Which is not the assembler directive?
Option A:	NOP
Option B:	EQU
Option C:	DQ
Option D:	END
10.	How many dedicated interrupts are present in 8086 processor?
Option A:	5
Option B:	2
Option C:	32
Option D:	256
11.	How many ICW (Initialization Control Words) are in 8259?
Option A:	3
Option B:	4
Option C:	5
Option D:	6
12.	IC 8255 is _____.
Option A:	PIC
Option B:	PPI
Option C:	PIT
Option D:	DMA
13.	8254 has _____ counter.
Option A:	1

Option B:	2
Option C:	3
Option D:	4
14.	In slave mode of DMA _____ function as a bus master.
Option A:	Bus Controller
Option B:	DMA
Option C:	Microprocessor
Option D:	IO device
15.	Number of Channel within 8257 are ____ .
Option A:	4
Option B:	5
Option C:	6
Option D:	8
16.	ISR block in 8259 stand for _____ .
Option A:	In Service Register
Option B:	Interrupt Service Register
Option C:	Interrupt Subroutine Register
Option D:	Input Subroutine Register
17.	80386DX is _____ processor.
Option A:	8
Option B:	16
Option C:	32
Option D:	64
18.	Descriptor register of 80386DX processor is ____ byte register.
Option A:	2
Option B:	4
Option C:	8
Option D:	16
19.	Pentium processor maximum virtual memory address space is ____ .
Option A:	4 GB
Option B:	8 GB
Option C:	4 TB
Option D:	64 TB
20.	Pentium Processor is ____ bits processor.
Option A:	128
Option B:	64
Option C:	32
Option D:	16
<b>Descriptive Questions</b>	

Q2.	Solve any FOUR
A	Write short note on Bus controller.
B	Explain String instructions of 8086 with example.
C	Draw format for ICW3 of 8259
D	Draw Block Diagram of 8255
E	Explain Eflag register of 80836DX processor.
F	Explain instruction pairing rule in Pentium processor.
Q3.	Solve any TWO
A	Draw architecture of 8086 processor.
B	Explain addressing modes of 8086 processor.
C	Draw and explain block diagram of programmable Interval timer.
<b>All the Best!!!</b>	