PRATIKSHA S. PATIL

M.E. Electronics & Telecommunication Engg., S.P.I.T, Mumbai University
B.E. Electronics & Telecommunication Engg.,SJCET, Mumbai University

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Email Id	: **pratiksha.patil@mctrgit.ac.in**

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Designation	: Assistant Professor in Electronics & Telecommunication Department.
Experience	: Educational 2 Years

Date of Joining	: 1/07/2017

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Areas of Interest	: Signal Processing, Wireless Communication, Microwave, IoT (Internet of Things)

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Publications

Conferences: 02

(1) **Pratiksha S. Patil** , Jinalkumar Doshi , Dayanand Ambawade ,"Reducing Power consumption in Smart Device by Proper Management of Wakelock" is published in the 2015 IEEE International Advance Computing Conference (IACC), Bangalore. (Page- 883-887).

(2) **Pratiksha S. Patil** , Jinalkumar Doshi, Zalak Dave, Ganesh Gore, Jonathan Joshi, Reena Sonkusare, Surendra Rathod "Implementing a Cloud Based Xilinx ISE FPGA Design Platform for Integrated Remote Labs" is published in the 2015 International Conference on Advances in Computing, Communications and Informatics(ICACCI), Kochi (Page-533-537).

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Professional Bodies Association:

- Life Member of "I.E.T.E"

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Others

- Completed NPTEL 12 week course on Linear ICs, Mosfets, ICs and Applications in Jan - April 2018
- Organized National Seminar on "Recent Trends in VLSI" at SJCEM in May 2017.
- 1Year Industry experience as Android Developer.
- Project on Sensor network using Zigbee Technology in B.E
- M.E Thesis on "Reduction of Power Consumption in Smart Device"
- Certificate Course in "Mobile application development in Android".
- Participated in the Texas Instrument Analog Maker Competition 2014 at SPIT
- Attended the Mini-DebConf Mumbai 2015 at IIT Bombay
- Attended one week STTP on "Cyber Security, Software Testing and Emerging Trends in Raspberry Pi"

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