Full Name:
MRS. DIPSHRI HEMANT KALE

Qualification:
M.E. Electronics & Telecommunication, Mumbai University
B.E. Electronics & Telecommunication, Amravati University

Residence: B602, Pradhnya CHS ,New MHB colony ,Gorai Road,Borivali west,Mumbai 91
Email-Id: dipshri.kale@mctrgit.ac.in

Designation: Assistant Professor in Electronics & Telecommunication Department.

Experience: Educational 13 Years, Industry 01 Years

Date of Joining: 18th July 2022

Areas of Interest:
Communication Network, Optical fiber Communication

Publications

Conferences:

1. International conference on Emerging Trends in Applied Engineering ane Technology held in KGCE karjat on 1st and 2ndapril 2016 publish paper on "5 G technology for wireless communication".

2. Publish paper in International paper presentation held in YTITCOE karjat on 6 and7 march 2013 ,"Design of Perceptual video encryption".

3. First prize in National Level Paper Presentation held in COET Bhambhori Jalgaon in march 2011,Title:"Cryptography for internetsecurity using AES"

4. Publication of manuscript title" PARAMETER ANALYZED BY AD8302CHIP" in GLOBAL JOURNAL OF ENGINEERING SCIENCE AND RESEARCHES, Feb 2016, ISSN 2348-80342

5. First prize in National Level Paper Presentation held in COET Bhambhori Jalgaon in march 2011,Title:"Cryptography for internetsecurity using AES"

Journals:

1. Publish paper in international journal of Advances in Engineeringand Applied science on "INTERCOMMUNICATED ANTICOLLISION SYSTEM"Vol-1,2014,ISSN(p)2348-1811,ISSN(E)2348-182Xpage-No:18-20

2. Publish paper in international journal of Advances in Engineeringand Applied science on "ROUTE DESIGN OF ECO-FRIENDLY INDUSTRIES"Vol-1 ISS-2,2014,ISSN(p)2348-1811,ISSN(E)2348-182Xpage-No:142-147

STTP and FDP :

1. Attended AICTE sponsored National level Teacher Training Workshop on "Student Induction Program from 17-21 july 2017 at VIT Pune.
2. Attended AICTE sponsored National level STTP on "VLSI andEmbedded Systems" organized by EXTC department of FRC Rodrigues institute of technology Vashi
3. Attended ISTE approval STTP on "Image Processing,Artificvial Intellegence and its Application " during 18-27 Nov 2013 at HVPM COET Amravati.
4. Attended Workshop for Embrdded microchip technology in may2013 at YTITCOE. Karjat
5. Two times achieve Appreciation award for Examiner for National Level Paper presentation held in Manohar Phalke Diploma Engineering Collage in 2012 and 2013