

SAMRIN MOHD SAMEER PABREKAR

Mobile: +91 9821596279

E-mail: samrin.ansari71@gmail.com

Personal Details

Address: 802, Modern Enclave,
R.B Chandorkar Marg,
Agripada.Mumbai-400011.

Gender: Female

Nationality: Indian

Marital Status: Married

Date of Birth: 19 August 1991.

SUMMARY

- A hardworking Lecturer with 8 years of experience in teaching Electronics & Telecommunication Engineering subjects with strong conceptual knowledge in Microcontroller and Embedded System.
- Currently allied with Rajiv Gandhi Institute of Technology, Versova Mumbai as Assistant Professor in the Department of Artificial Intelligence and Data Science.
- Expertise in delivering the work assigned within the agreed time with superior quality of work.
- Possess demonstrated excellent communication, organizational and time management skills.

AREAS OF EXPERTISE

- NBA documentation
- Career Guidance & Counseling
- Member of core committee of Admission Work since 2015 till date.
- Member of RAC for MSBTE Examination.
- Assessor and moderator for MSBTE Examination.
- Understudy for Mumbai University Examination
- Guiding students with innovative projects
- Concepts of VLSI Technology
- Concepts of Nanowire Technology
- Concepts of Embedded System
- Concepts of Microcontroller & Programming
- Concepts of Digital Techniques

EDUCATION

Qualification	University/Board	Name of College	Month & Year of Passing	Score
M.E (Electronics & Telecomm)	Mumbai	Vidyalankar Institute of technology	January 2017	7.57 CGPI
B.E (Electronics)	Mumbai	M.H. Saboo Siddik College of Engineering	May 2013	66.12%
HSC	Maharashtra State Board, Pune	Sophia College	Feb 2009	74%
SSC	Maharashtra State Board, Pune	St. Joseph's High School	March 2007	85.07%

ACADEMIC PROJECT WORK

Successfully completed & submitted a thesis report titled as, '**Design Optimization of Nanowire MOSFET Device at Sub Micron Technology**' during the ending academics of M.E. Electronics & Telecommunication.

Project Frame work

- ✓ Conducted literature review to study the Short Channel Effect on the existing Silicon Nanowire Device and Emerging High-K materials.
- ✓ Study of Effect of variation of Channel Diameter and Metal Gate function on various Electrical Parameter.
- ✓ Performed Electrical Characterization of Nanowire device test structures and improved model for to avoid Short Channel Effects.
- ✓ Design and Development of Cylindrical Channel Horizontal 22nm Nanowire in Visual TCAD Simulator.
- ✓ Study of impact of High-K on Optimized Nanowire Structures.

TECHNICAL COMPETENCIES

- ✓ Hands on experience of designing 3D Nanowire Device Structure using TCAD.
- ✓ Python Coding for Simulation of 22nm nanowire.
- ✓ Hands on experience on Programming 8051 using Keil μ Vision.

CERTIFICATION

- ✓ Holding the Participation Certificate in Poster Presentation at the First National Research Symposium by Vidyalankar Institute of Technology.
- ✓ Holding the Certificate of Excellence, being awarded Consolation prize in Post Graduate Category in Poster Presentation at the First National Research Symposium by Vidyalankar Institute Of Technology
- ✓ Holding certificate of participation in the 2-days Workshop conducted by Cadre Design System at Vidyalankar Institute Of Technology on “Visual TCAD (VTCAD-2016) and Genius Device Simulator”.

PAPER PUBLICATION

- Ansari Samrin, Sangeeta Joshi, “Nanowire: An Innovative Approach For 22nm Technology Node”, poster in First National Research Symposium on RACEM -2015, VIT Mumbai.
- Ansari Samrin, Ashish Bait, Subha Subramaniam, Sangeeta Joshi, Geetha Narayan, R.N. Awale “Design Optimization of Nanowire Transistor with Impact of High-K at sub-22nm”, in The 3rd International Conference on Emerging Electronics (ICEE 2016) organized by IIT Bombay.

KEY DELIVERABLES

➤ **At M.H.Saboo Siddik Polytechnic (Electronics & Telecommunication Dept)**

- To involve in the admission work to guide students and their parents with the Centralized Admission Process (CAP) and other related activities.
- To involve in the process of examination as an invigilator to ensure proper conduct of exams.
- To involve in the academic work regarding activities of the National Board of Accreditation (NBA).
- To involve in the process of examination as QC in charge.
- To impart knowledge to the students in the core subjects of Electronics Engineering to clear the concepts essential for working in a industrial environment as well as for higher studies.

- To guide and counsel students to help them choose an appropriate area of study in accordance with the student's area of interest.
- To carry out result analysis of the student in order to decide the future course of action for betterment of results.
- To organize industrial visits and guest lectures by industry experts to keep students aware of recent trends in technology.
- **Subjects Taught: Embedded Systems, Microcontroller & its Application, Basic Electronics, Digital Circuits and Technique, Electronics Device and Circuits.**

➤ **At Rajiv Gandhi Institute of Technology (Artificial Intelligence & Data Science Dept)**

- Miniproject Coordinator for Second Year students of Artificial Intelligence & Data Science
- Student Mentoring Coordinator for the Department.
- Member of institute NAAC Committee for Criteria 7
- Time Table and Load Distribution Coordinator of the Department.
- Understudy for Mumbai University Examination Nov-23
- **Subject Taught: Computer Graphics (S.E.), Computer Network (T.E.).**

WORK EXPERIENCE

Organization	Designation	Duration
Electronics Regional Test Lab	Apprentice	Nov 2013 till March 2014
M.H.Saboo Siddik Polytechnic	Visiting Lecturer (Computer Engg.)	Dec 2015 till March 2017
M.H.Saboo Siddik Polytechnic	Visiting Lecturer (Electronics & Telecomm Engg.)	Feb 2017 till April 2017
M.H.Saboo Siddik Polytechnic	Lecturer (Dept. of Electronics & Telecomm Engg.)	June 2017 till May 2023
M.H.Saboo Siddik Polytechnic	I/c HOD, Civil Engg (B) Dept.	June 2022 till May 2023
Rajiv Gandhi Institute of Technology, Mumbai	Assistant Professor (Dept. of Artificial Intelligence and Data Science)	August 2023 till date

LANGUAGES:

- Fluent in English
- Native: Marathi, Hindi, Urdu

Place: Mumbai